

100E80"66E4660

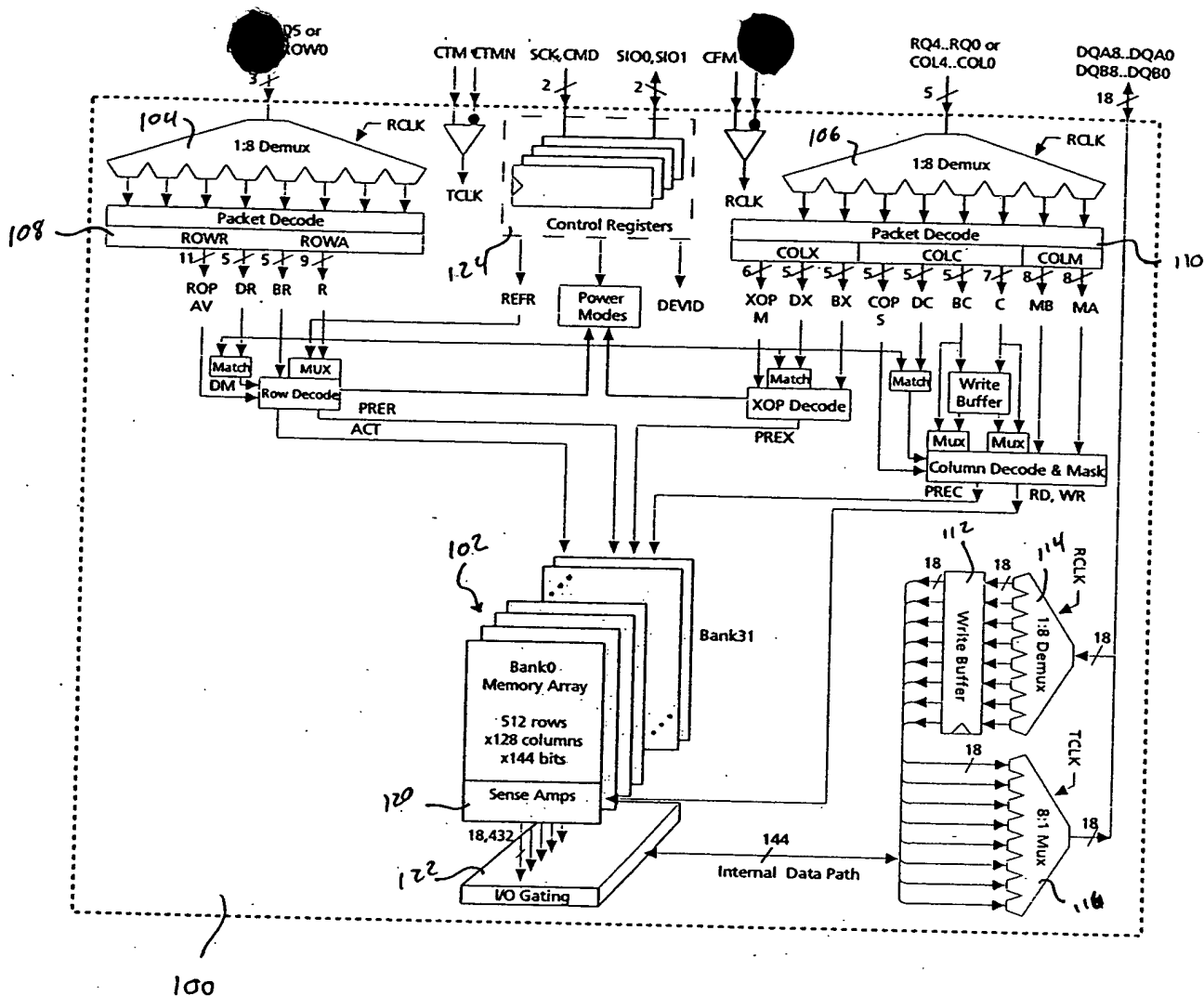


Fig. 1

150

(TOP VIEW)

10	V _{DD}	GND		V _{DD}	GND	V _{DD}					V _{DD}	V _{DD}	V _{DD}		GND	V _{DD}
9																
8	V _{DD}	CMD	V _{DD}	GND	GNDa	GNDa	V _{DD}	V _{DD}	GND	GND	V _{DD}	V _{DD}	GND	GND	V _{CMOS}	V _{DD}
7	DQA8	DQA7	DQA5	DQA3	DQA1	CTMN	CTM	RQ7	RQ5	RQ3	RQ1	DQB1	DQB3	DQB5	DQB7	DQB8
6																
5																
4	GND	DQA6	DQA4	DQA2	DQA0	CFM	CFMN	RQ6	RQ4	RQ2	RQ0	DQB0	DQB2	DQB4	DQB6	GND
3	GND	SCK	V _{CMOS}	GND	V _{DD}	GND	V _{DDa}	V _{REF}	GND	V _{DD}	GND	GND	V _{DD}	SIO0	SIO1	GND
2																
1	V _{DD}	GND		GND	V _{DD}	GND						GND	GND	GND		V _{DD}
	A	B	C	D	E	F	G	H	I	J	K	L	M	N	P	S

Fig. 2